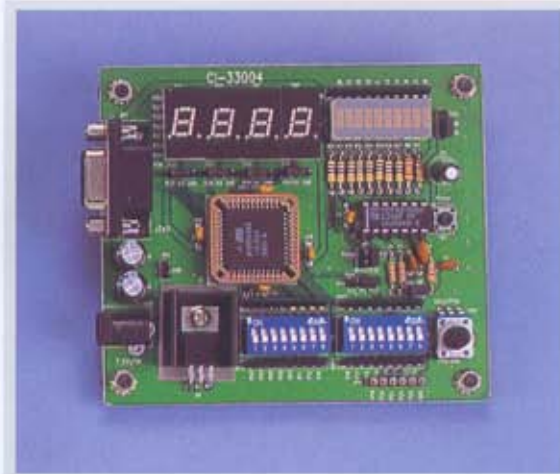


CIC-33004

CIC-33004

EXPERIMENT BOARD



Experiment Board

1. Adopt Atmel ATF1504-15 FPGA chip (compatible with Altera MAX 7064), containing 64 Microcells (over 1000 usable gates) and able to reprogram about 10k times
2. Using Altera MAX+PLUS^{II} for chip development. Users can use graphic or text editor (HDL syntax) to design, simulate and implement digital circuit easily
3. The program is downloaded from PC to FPGA chip via series port with JTAG technology
4. Providing some simple I/Os for design efficiency
5. Suitable for new FPGA designers
6. Best solution for the shortage of budget

Specification

1. 16 DIP switches for digital state input
2. 10-BAR LEDs for output state display
3. 4-digit 7-segment display for static and dynamic driving operation
4. 2 channels clock pulse output
Adjustable frequency range: 10Hz to 350Hz ($\pm 20\%$)
fixed frequency: 3.5 KHz ($\pm 20\%$)
5. Altera MAX+PLUS^{II}
6. Altera file transfer software POF2JED and ISP programming software
7. For Windows 95/98/2000/XP

Application Fields

1. Designing combinational logic circuits
2. Designing sequential logic circuit
3. Designing flip-flops circuit
4. Designing counters and applications
5. Designing ALUs and applications
6. Encoder/decoder and multipliers/demultipliers
7. Production and application of digital subjects

Accessories

1. ByteBlaster
2. Serial cable
3. Power adaptor
4. Operation manual